

	1	2	3	4	5		6		7		3	
	RoHS Compliant						REVISIONS]	
	•							REV.	DESCRIPTION	DATE	DESIGN	
A		$\frac{\text{Connector Cer}}{2-0.70} \xrightarrow{(0.00){0.0}{1.0}}{0.0}$	<u>4.16</u> <u>0.90</u>			1.MATERIA 1.1 Inst	L: ulator:High	ecification Temperature Ther	moplastic,		A	
В		13.83 2-3.33 EId		10 1.595 2.145 5.10 3.010+7 040 13.52 7.40 0.70	13.83		 1.3 She 2.Plating: 2.1 Con 2.2 She 3.Property 3.1 Cur 	tact: Pho: II: SUS301 tact: Plate Plate II: Plated : rent Ratin	ck 94V–0. sphor Bronze(C521 –3/4H T=0.10m ed 30u''min Ni Ove d 1u"min Au Selective d 1u"min AU over Ni 1u"min AU overII g :0.5A AC/DC mc	m erall on solder o 30u''min N	rea area li	В
C		2. 2. 2. 2. 3. 2. 3. 1.10 0.32 0.	P5 P4 P3 P2 P1 3.66 .40	9 9 1.14 PIN M PIN M PIN M PIN M P2 P3	DAT2 DAT3/CD CMD		3.2 Volt 3.3 Aml 3.4 Aml 3.5 Cor 3.6 Insu 3.7 Dur	age Ratin pient Tem pient Hum Itact Resis ulation Res ability:500	g :12V AC/DC perature Range :-2 idity Range :95% F stance:100mΩ max. sistance:1000MΩ m	20°C~+60° R.H. Max.		С
			MEND P.C.B LAYOUT ral tolerance ±0.05) BITION AREA	P4 P5 P6 P7 P8 P9 P10	VDD CLK VSS DATO DAT1 GROUND GROUND		CARD (STAT SWITCH (CIRCUIT) DETECT SWITC	us) WITHOUT				
D				P11 P12 P13 P14	DETECT LEVER DETECT LEVER GROUND GROUND	DESIGN:	DATE:	ITI				- D
		te Technolo		.XXX ± 0.15 .	X. ± 5 [•] X [•] ± 2 [•] XX [•] ± 1 [•]	I.L Checked: C.W Approved:	2017. 3. 1 Date: 2017. 3. 1	M	Micro SD 1.28	H P/P CAR	SIZE:	
	REV: UNIT m	m SCALE 1=1		Vebsite:www.bsc-elec.co Email:bestcreate@bsc-		APPROVED: C.W	DATE: 2017. 3. 1	FI	NSH: DWG NO: M	SD-08XX5R-00	5 2 OF 3	
	1	2	3	4	5		6		7	{	}	-

